

AMENDMENTS TO THE SPECIFICATION

Please amend paragraph [0049] as follows:

**[0049]** In step 701, the source logic is assigned to a first logic block in the PLD, and in step 702, the destination logic is assigned to a second logic block in the PLD. Steps 701 and 702 can be performed, for example, by a single placement module 710. Placement module [[701]] 710 can be, for example, similar to placement software now in common use for the placement of PLD designs. Steps 701 and 702 can be performed sequentially (in any order) or concurrently. In some embodiments, steps 701 and 702 are performed in an interactive fashion, i.e., with the placements of the two blocks being optimized one with the other. Such placement processes are well known to those of skill in the art.